

Substitute for Form 1449A/PTO (Modified) (use as many sheets as necessary)		Attorney Docket No.: 16820.P279	Application Number: Not yet assigned 10802519
		First Named Inventor: Dinesh Annayya	
		Filing Date: Herewith	
OTHER ART - NO PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	Translation ²
lll	—	Louis E. Frenzel, "Programmable Framer Chip Improves OC-48 Efficiency, Using virtual concatenation, a next-generation Sonet chip allocates bandwidth dynamically, facilitates provisioning, and preclassifies and tags packets to free up network processors.", Electronic Design April 16, 2001, 4 pages.	—
lll	—	Nilam Ruparelia, "Delivering Ethernet over Sonet using Virtual Concatenation", CommsDesign, February 25, 2002, 8 pages.	—
lll	—	Michael Timothy Moore, "Filling the WAN-communications-line card", www.ednmag.com , April 18, 2002, p. 83-90.	—
lll	—	"A Brief Explanation of CRC Computation", http://www.ece.msstate.edu/~reese/EE4743/crcexplained.htm , downloaded 1/16/04, 2 pages.	—
lll	—	Eric E. Johnson, "High-Speed Computation of Cyclic Redundancy Checks", NMSU-ECE-95-011, November 1995, 14 pages.	—

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U.S. PATENT DOCUMENTS						
Exam. Initial*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number Code ²	Kind (If known)			
llp		6,519,737	—	Derby	2/11/03	—
llp		6,519,738	—	Derby	2/11/03	—

FOREIGN PATENT DOCUMENTS								
Exam. Initial*	Cite No. ¹	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD- YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
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